

Emerging Technologies in Solid State Devices Workshop
December 5 - 6, 2009
Marriott Baltimore Inner Harbor at Camden Yards
11am – 9pm

The SEMATECH Emerging Technologies in Solid State Devices Workshop will be held on Saturday and Sunday, December 5-6, the weekend before the IEEE International and Electron Devices Meeting. The workshop will be held at Marriott Baltimore directly across the street from where the IEDM will be hosted.

This year's workshop will include an audience of senior technologists, executives, and leading faculty members. We have three sessions this year, Emerging Memory Technologies, Energy Efficient Devices and High Mobility Channel Transistor.

Having gathered an array of researchers in each of the areas, we anticipate an informative set of talks, and lively discussions among the panelists and audience, resulting in a productive two days for all involved. See the preliminary agenda below. A more detailed agenda will be sent to all invited within the week.

Lunch and dinner will be provided.

Saturday, December 5

Emerging Memory Technologies

Raj Jammy, SEMATECH

Kirk Prall, Micron

Mike Kozicki, AIST

Rene Meyer, Unity Semi

Paul Kirsch, SEMATECH

Hyunsang Hwang, GIST

Philip Wong, Stanford University

Alex Ignatiev, University of Houston

Wei-Chyung Wang, University at Albany

Daniele Lelmini, Politecnico di Milano University

Mike Kozicki, Arizona State University

Sanjay Bannerjee, University of Texas at Austin

Matt Nowak, Qualcomm

Saied Tehrani, Everspin

Sayeef Salahuddin, University of California, Berkeley

Eugene Chen, Grandis

Krishna Saraswat, Stanford University

Sunday, December 6

Energy Efficient Devices

Chenming Hu, University of California, Berkeley

Tsu Jae King, University of California, Berkeley

Casey Smith, SEMATECH

Wim De Groot, Qualcomm

Rob Van Schaijk, Holst Centre/IMEC

Bruce White, SUNY at Binghamton University

Amit Lal, Cornell University

Qingkai Yu, University of Houston

Kaustav Banerjee, University of California, Berkeley

Sunday, December 6

High Mobility Channel Transistors

Robert Chau, Intel

Ghavam Shahidi, IBM

Jesus del Alamo, MIT

Mark Rodwell, University of California, Santa Barbara

Krishna Saraswat, Stanford University

Yee-Chia Yeo, National University of Singapore

Matty Caymax, IMEC

Peide "Peter" Ye, Purdue University

Edward Chang, Nanyang Technological University

Rainer Beccard, AIXTRON